



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/976,701

Filed: 10/12/2001

Inventor(s):

Michael C. Dorsey

Title: Memory BIST Employing a  
Memory BIST Signature

§ Examiner: Trimmings, John P

§ Group/Art Unit: 2133

§ Atty. Dkt. No: 5681-55300

§ I hereby certify that this correspondence is being deposited with  
§ the United States Postal Service with sufficient postage as first  
§ class mail in an envelope addressed to Commissioner for  
§ Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the  
§ date indicated below.

§ Erik A. Heter

§ Printed Name

§ August 11, 2004

§ Signature

§ Date

**RESPONSE TO OFFICE ACTION OF  
May 12, 2004**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of May 12, 2004, to  
further highlight why the application is in condition for allowance.

Please amend the case as listed below.

**IN THE DRAWINGS:**

Please amend Figures 1, 2, and 9 in accordance with the replacement sheets attached hereto.